



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2815
Examiner: Edward Joseph Wojciechowicz

In Re PATENT APPLICATION of:

Applicant(s): Katsuhiro HISAKA

Serial No.: 10/673,392

Filing Date: September 30, 2003

For: FUSE CIRCUIT AND SEMICONDUCTOR
DEVICE INCLUDING THE SAME

Atty. Dkt.: IIZ 138

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) **AMENDMENT**
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October 26, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Examiner's Action mailed on June 30, 2005, the period for response to which is extended by a Petition for a One-month Extension of Time filed herewith, please amend the above-identified application as follows: